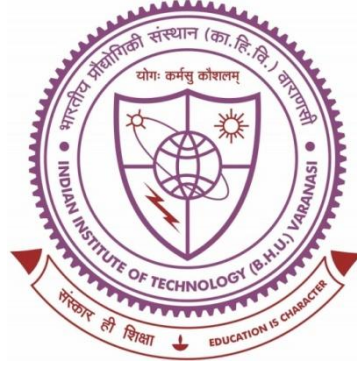


**“GATE INTERFACE STUDY FOR THE IMPROVEMENT OF ION-  
CONDUCTING DIELECTRIC BASED LOW OPERATING VOLTAGE  
THIN FILM TRANSISTOR ”**



**Thesis submitted in partial fulfillment for the  
Award of Degree**

**Doctor of Philosophy**

**by**

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