

UNDERTAKING

I, Divya Somvanshi, certify that the work embodied in this Ph.D. thesis is my own bonafide work carried out by me under the supervision of Prof. S. Jit for a period of four years and six months from 09 August 2010 to 01 Feb 2015 in the Department of Electronics Engineering, Indian Institute of Technology (Banaras Hindu University), Varanasi. The matter embodied in this Ph.D. thesis has not been submitted for the award of any other degree/diploma.

(Divya Somvanshi)



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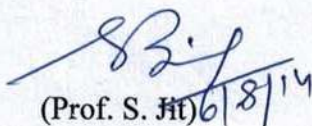
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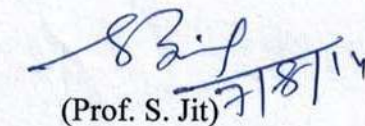
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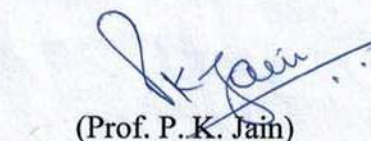
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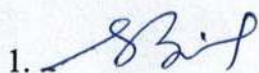
This is to certify that **Miss Divya Somvanshi**, Ph.D. Scholar, Department of Electronics Engineering, Indian Institute of Technology (Banaras Hindu University), Varanasi has delivered her Ph.D. Pre-Submission Seminar on her Ph.D. thesis entitled "**Some Studies of ZnO Nanostructure Based Schottky and Heterojunction Devices Fabricated on Silicon Substrates**" on 6th August, 2014 at 11:30 P.M. in the Committee Room of the Department as per the satisfaction of RPC and DPGC members.


(Prof. S. Jit) 6/8/14
Supervisor

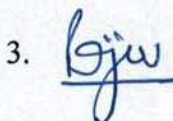

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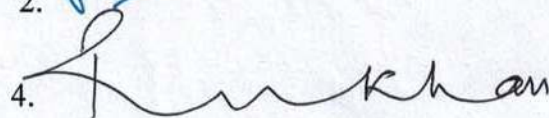

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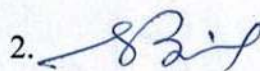
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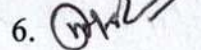
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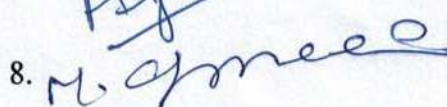
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Title of the Thesis: **“Some Studies of ZnO Nanostructure Based Schottky and Heterojunction Devices Fabricated on Silicon Substrates”**

Candidate’s Name: Divya Somvanshi

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