



विद्युत अभियांत्रिकी विभाग  
भारतीय प्रौद्योगिकी संस्थान (काशी हिन्दू विश्वविद्यालय) वाराणसी


DEPARTMENT OF ELECTRICAL ENGINEERING  
Indian Institute of Technology (Banaras Hindu University) Varanasi

### Undertaking from the Candidate

I, **Amit Kumar Choudhary**, declare that I have pursued my research work under the Supervision of Prof. S. K. Nagar from September 2011 to June 2017. I hereby submit my thesis entitled "**Model Order Reduction of Discrete-time Interval Systems**" for the award of Ph.D. degree.

Date: 7/6/17

Place: Varanasi

  
(Amit Kumar Choudhary)



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**Course Work/Comprehensive Examination  
Completion Certificate**

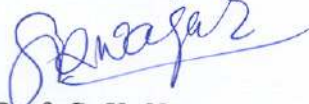
This is to certify that Mr. Amit Kumar Choudhary (Term No. RAC/RES/July11/T-62 Enrollment No. 340522), a bonafide research scholar of Electrical Engineering department, has satisfactorily completed the course work/comprehensive examination requirement which is a part of his Ph. D. programme.

Date: 7/6/17  
Place: Varanasi

(Head of the Department)

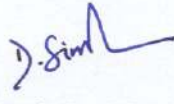
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Forwarded by:

  
Prof. S. K. Nagar

(Supervisor)  
आचार्य / PROFESSOR

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Prof. D. Singh

(DPGC, Convener)  
Convener, DPGC

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### Pre-Submission Seminar Completion Certificate

This is to certify that Sri. Amit Kumar Choudhary (Term No. RAC/RES/July11/T-62 Enrollment No. 340522), a bonafide research scholar of Electrical Engineering department, has satisfactorily completed the pre-submission seminar on the topic "**Model Order Reduction of Discrete-time Interval Systems**" on Monday, 01-May-2017 which is a part of his Ph. D. programme.

Date: 7/6/17  
Place: Varanasi

(Head of the Department)

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Title of the Thesis: Model Order Reduction of Discrete-time Interval Systems

Candidate's Name: Amit Kumar Choudhary

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